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APPLICANTS

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**** CONTINUING DATA *******
~~A~~ **NONE**

**** FOREIGN APPLICATIONS *******
~~A~~ JAPAN P2000-027365 01/31/2000

IF REQUIRED, FOREIGN FILING LICENSE
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 12
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after				
Verified and Acknowledged <i>SUDHANSHU PATHAK</i> Examiner's Signature	Initials			

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TITLE

Timing error detection circuit, demodulation circuit and methods thereof

FILING FEE RECEIVED 1502	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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